

Fig. 2B

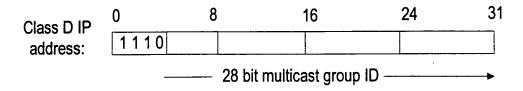


Fig. 3A

0	4	1	8		16	31
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	32 bit Class D Address					
3	2	,				63

Fig. 3B

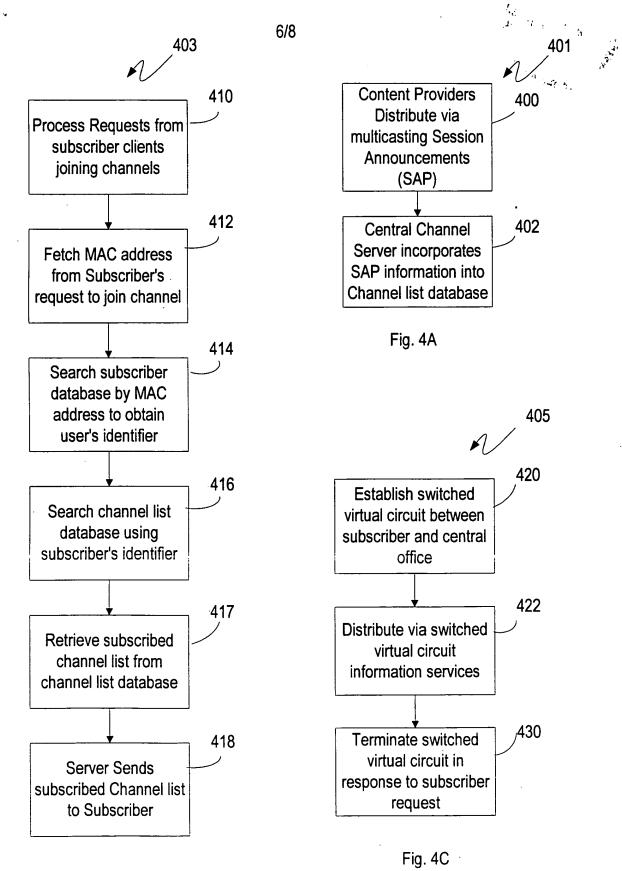


Fig. 4B

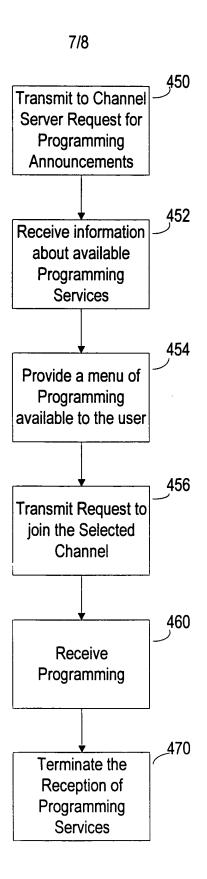


Fig. 4D

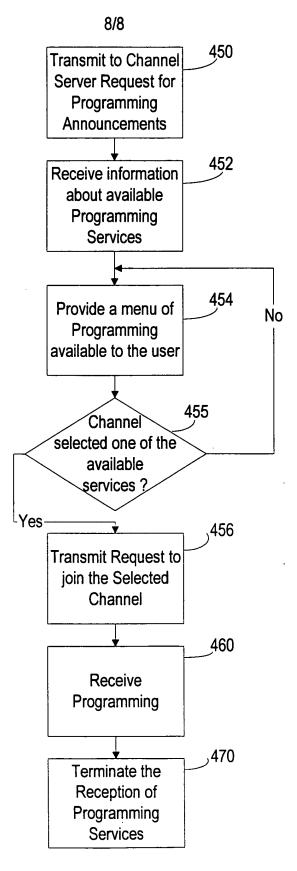


Fig. 4E